

## **IN THE CLAIMS**

Claims 1-64 are canceled.

65. [Previously Presented] A field emission display method comprising:

- providing a monolithic semiconductive substrate;
- providing a luminescent member spaced from and opposite the monolithic semiconductive substrate;
- forming a plurality of emitter regions using the monolithic semiconductive substrate;
- electrically isolating the plurality of emitter regions from one another;
- providing a plurality of emitters within individual ones of the emitter regions;
- providing a plurality of address circuits for respective ones of the emitter regions and individually comprising row circuitry and column circuitry;
- coupling individual ones of the address circuits with emitters of respective individual ones of the emitter regions;
- using the respective address circuits, providing an electrical potential across selected ones of the emitters of the respective emitter regions; and
- responsive to the electrical potential, emitting electrons from the selected emitters towards the luminescent member to generate an image.

66. [Previously Presented] The method of claim 65 wherein the providing the electrical potential comprises applying the electrical potential across different elevational portions of the selected emitters of the respective emitter regions.

67. [Previously Presented] The method of claim 65 further comprising:  
providing a vacuum intermediate the monolithic semiconductive substrate and  
the luminescent member; and  
passing the electrons through the vacuum towards the luminescent member after  
the emitting.

68. [Previously Presented] The method of claim 65 wherein the electrically  
isolating comprises etching the monolithic semiconductive substrate to define the  
emitter regions.

69. [Previously Presented] The method of claim 65 wherein the providing the  
emitters comprises forming the plurality of emitters to comprise bulk substrate material  
of the monolithic semiconductive substrate.

70. [Previously Presented] The method of claim 65 wherein the luminescent  
member comprises a face plate.

71. [Previously Presented] The method of claim 65 wherein the luminescent  
member comprises a phosphor material configured to generate the image responsive to  
the reception of the electrons.

72. [Previously Presented] The method of claim 65 wherein the address circuits are individually configured to address the emitters of individual ones of the respective emitter regions independent of others of the address circuits.

73. [Previously Presented] The method of claim 65 wherein the providing the emitters comprises etching bulk semiconductive material of the monolithic semiconductive substrate.

74. [Previously Presented] The method of claim 65 wherein the row circuitry and the column circuitry of an individual one of the address circuits comprise a plurality of address lines arranged orthogonal with respect to one another within the respective one of the emitter regions.

75. [Previously Presented] The method of claim 65 wherein the coupling of the address circuits with the emitters of the respective emitter regions comprises configuring individual ones of the address circuits to address the emitters of the respective emitter region independent of addressing of the emitters of others of the emitter regions using others of the address circuits.

Claims 76-110 are canceled.

111. [New] The method of claim 65 wherein the providing the emitters comprises forming the emitters within the emitter regions to comprise material of the monolithic semiconductive substrate.

112. [New] The method of claim 65 wherein the providing the address circuits comprises providing the address circuits comprising circuitry external of the monolithic semiconductive substrate.